

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	70146	ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/09 08:00
S2	22573	program\$5 with ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 10:30
S3	85	(program\$5 with ASIC).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 14:18
S4	2	"4694416".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 14:19
S5	100	heterogeneous with (ASIC FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 15:49
S6	6	S5 and ("717".clas. "716.clas")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:06
S7	0	"09".src. and ("815133" "997530").an.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 14:35
S8	3	"09".src. and ("815133" "997530").ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 14:36
S9	3	"09".src. and ("815122" "997530").ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 15:30

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S10	2	"20030200538".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 15:30
S11	28	heterogeneous with (ASIC FPGA) with program\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/01 15:49
S12	31281	FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/07 11:16
S13	560	FPGA same HDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/07 11:18
S14	377	FPGA with HDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/07 11:28
S15	10	("6286134" "6526570" "6292938" "5812851" "5854929").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/07 11:28
S16	201	notify and release and HDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/08 16:24
S17	5	(notify with output) and release and HDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/08 16:27
S18	6	(notify same release) and HDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/08 16:41
S19	2	"6496962".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/08 16:42

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S20	1199	count adj table	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/09 08:00
S21	127505	notify release with S20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/09 08:00
S22	8	(notify release) with S20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/09 08:01
S23	16	(US-20030102889-\$ or US-20030200538-\$ or US-20020138716-\$ or US-20020112091-\$ or US-20020133586-\$).did. or (US-7065481-\$ or US-6625797-\$ or US-6496962-\$ or US-7000213-\$ or US-6732354-\$ or US-6836839-\$ or US-6433578-\$ or US-5854929-\$).did. or (WO-3088095-\$).did. or (EP-1612940-\$ or US-20030200538-\$).did.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 08:27
S24	7	S23 and (notify release ready)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:42
S25	14252	(tim\$4 synchronization sync synchroniz\$4) near table	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:49
S26	49	((tim\$4 synchronization sync synchroniz\$4) near table) and hdl	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:44
S27	39	S26 and @ay<="2002"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:50
S28	5566	((tim\$4 synchronization sync synchroniz\$4) near table) and (notify release ready)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:49

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S29	360	((tim\$4 synchronization sync synchroniz\$4) near table) same (notify release ready)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:49
S30	128	((tim\$4 synchronization sync synchroniz\$4) near table) with (notify release ready)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:49
S31	88	S30 and @ay<="2002"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 09:50
S32	1	S30 and hdl	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 10:39
S33	4161	timing adj constraints	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 10:40
S34	487	(timing adj constraints) same (circuit gate)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 10:40
S35	538	(timing adj constraints) same (circuit gate hdl)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 10:40
S36	85	((timing adj constraints) same (table array)) same (circuit gate hdl)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 10:41
S37	23	((timing adj constraints) with (table array)) same (circuit gate hdl)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/11/09 10:41
S38	5104	717/114-119.ccls. 716/1,8,11,17. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:07
S39	0	S38 and (relese same buffer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:07

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S40	0	S38 and (release)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:07
S41	428	S38 and (release)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:07
S42	17	S38 and (release same buffer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:08
S43	675	S38 and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:08
S44	398	S38 and fpga and timing	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/07 11:09